



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. Of: TAKAHASHI
Serial No.: 09/867,098
Filed: May 29, 2001
For: DISPLAY DEVICE
Group: 2675
Examiner: UCHENDU O. ANYASO

DOCKET: NEC 01FN014

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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JUL 07 2003

Technology Center 2600

AMENDMENT A

Dear Sir:

This Amendment is being filed in response to the Official Action mailed March 28, 2003.

REMARKS

Turning to the Examiner's rejection of claims 1-3 and 8-10 under 35 USC § 102 as anticipated by Kasahara et al. (U.S. Patent No. 6,388,678), the Examiner's rejection is in error. Nowhere does Kasahara et al. teach or show a graduation circuit that utilizes both a diffusion circuit and a dither pattern circuit. Kasahara et al. teaches that there are instances in which a dither circuit is used, and other instances in which an error diffusing circuit is used in a spatial density changing circuit (col. 26, lines 31-34), and describes the dither circuit as a mode of spatial density changing circuit (col. 26, lines 35-37). "Mode" is defined by the American Heritage® Dictionary, Fourth Edition (entry 1.b) as being "a particular form, variety or manner" (copy of excerpt enclosed). Kasahara et al. simply discloses that either a diffusing or a dither circuit can be used, but not that both a diffusing circuit and a dither circuit are used.

In the rejection, the Examiner takes the position that Kasahara et al. teaches a second input terminal (HD, VD) that receives a digital signal. However, this assertion of the Examiner is in error. The input signal of this terminal is a vertical synchronizing signal VD and a

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